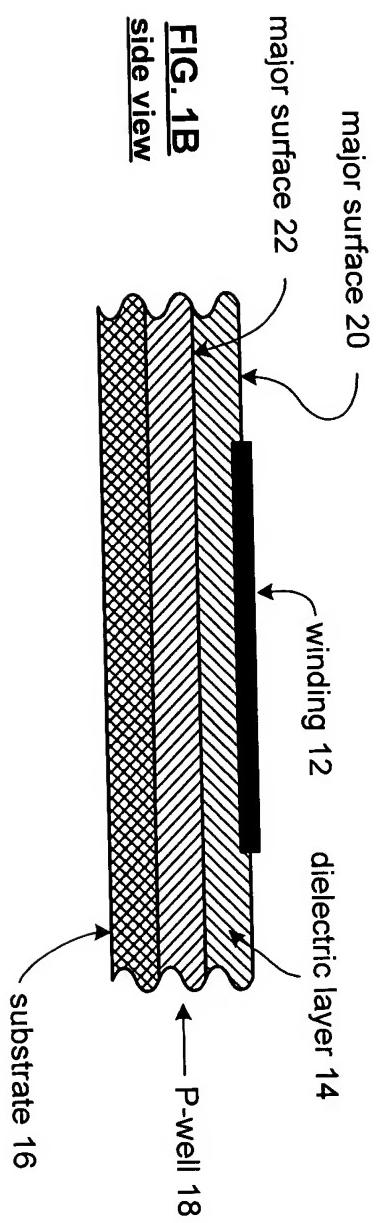
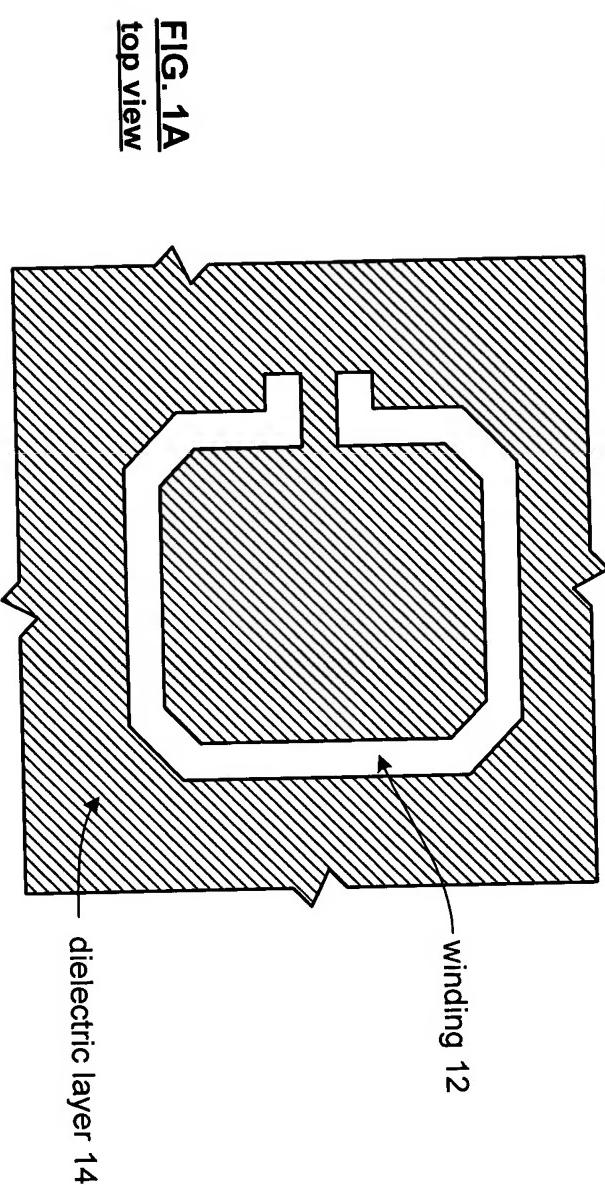
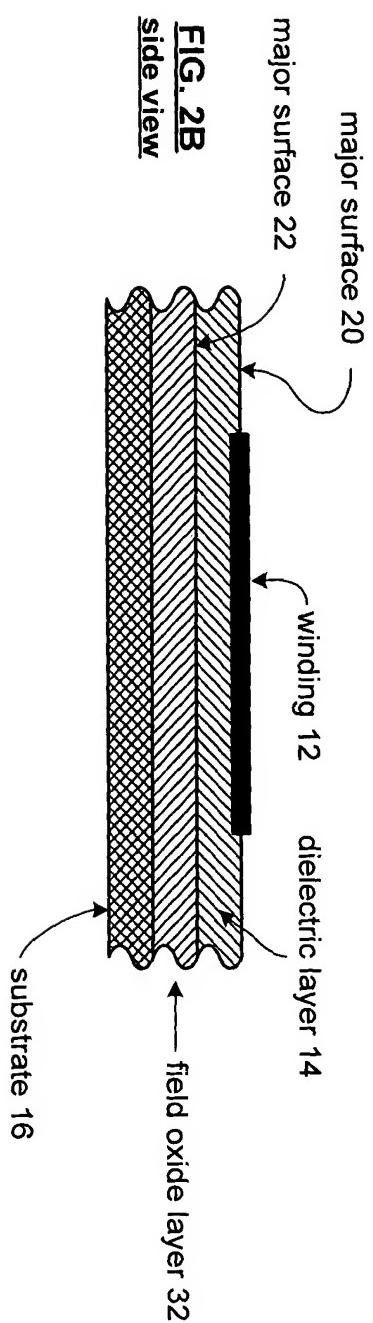
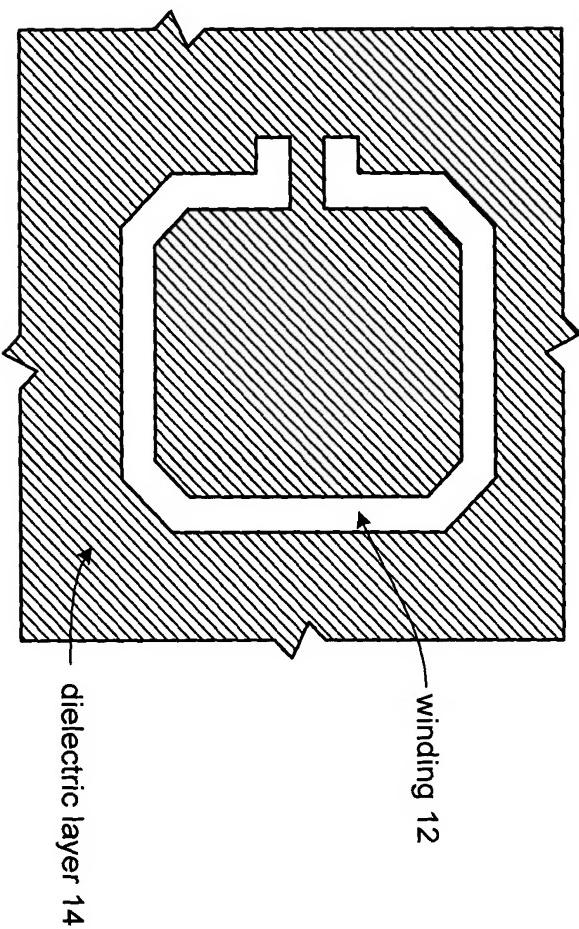


on-chip inductor 10

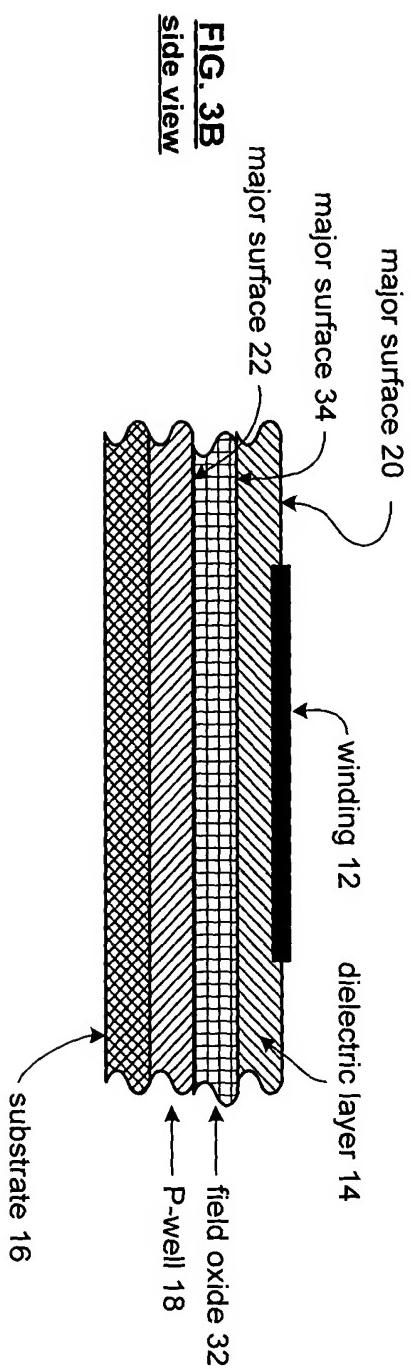
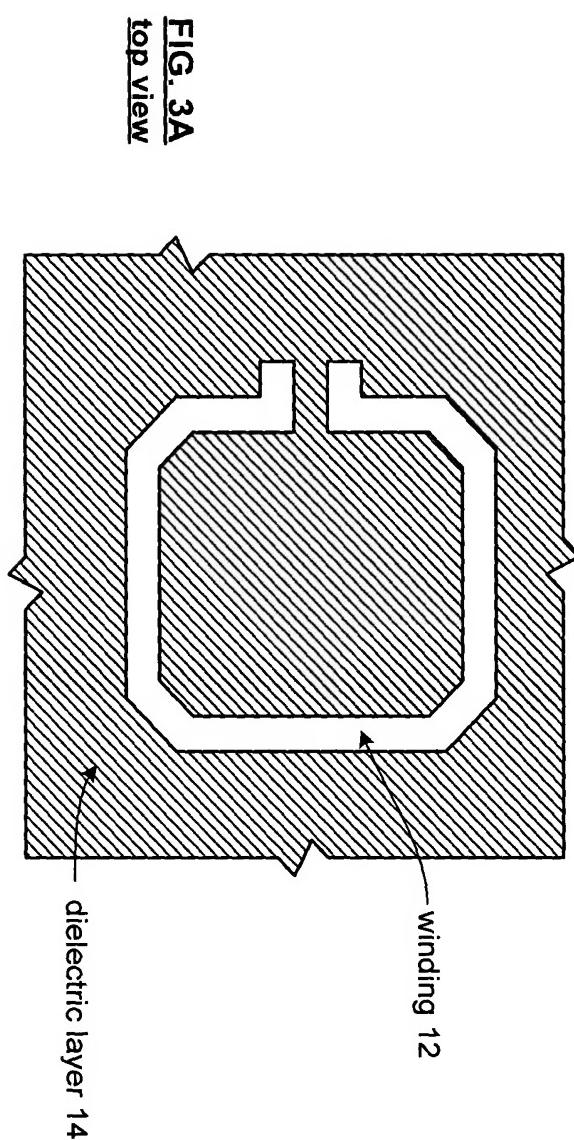


on-chip inductor 25

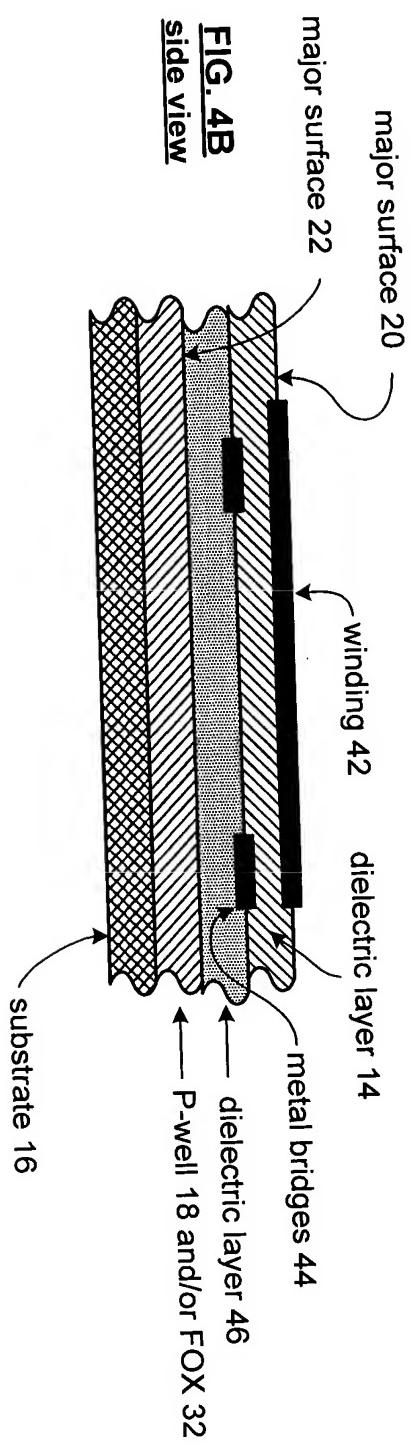
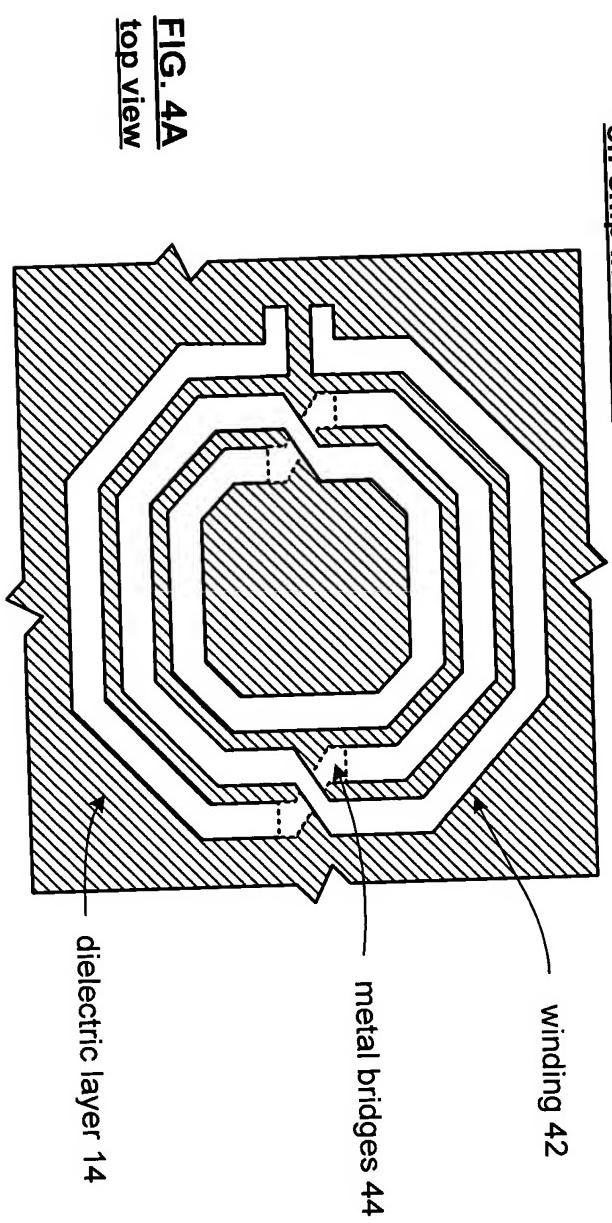
FIG. 2A
top view



on-chip inductor 30



on-chip inductor 40



differential inductor 60

FIG. 6A
top view

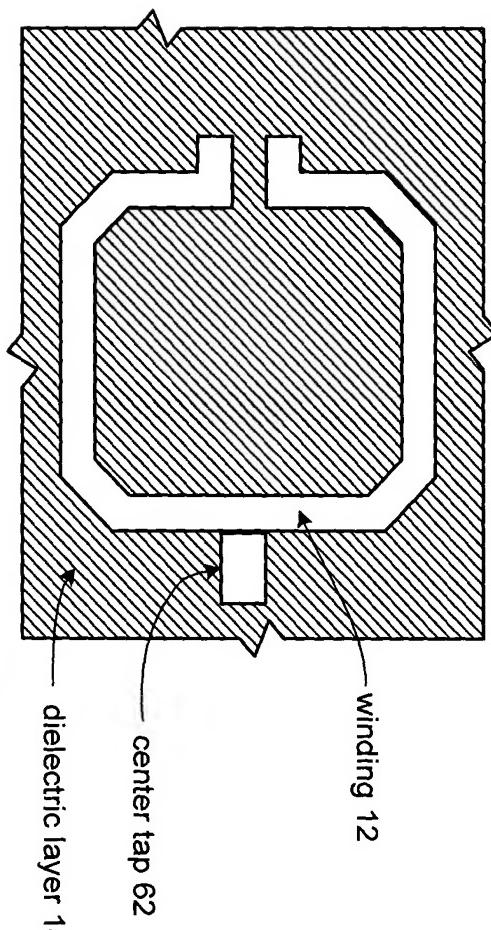


FIG. 6B
side view

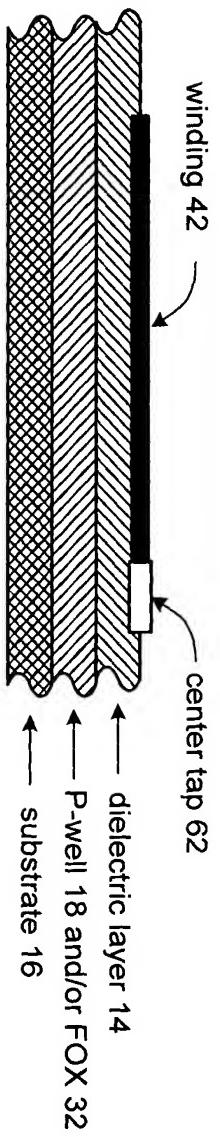
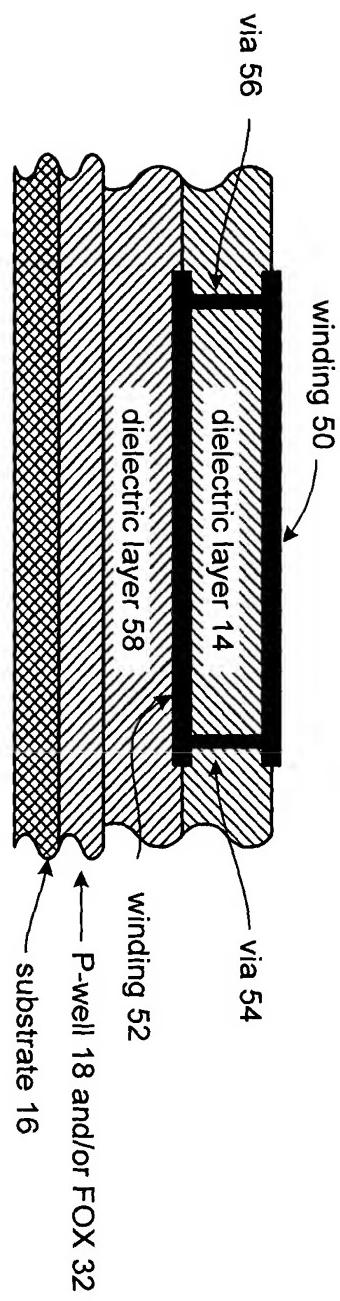
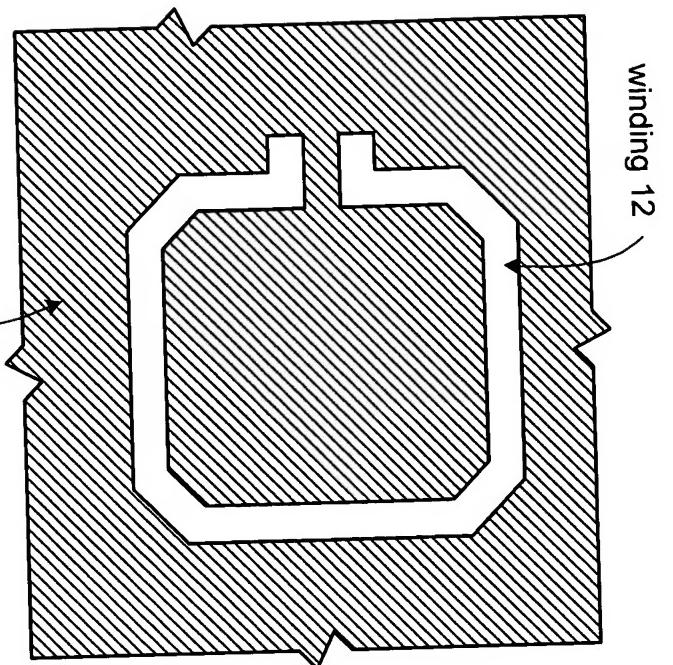
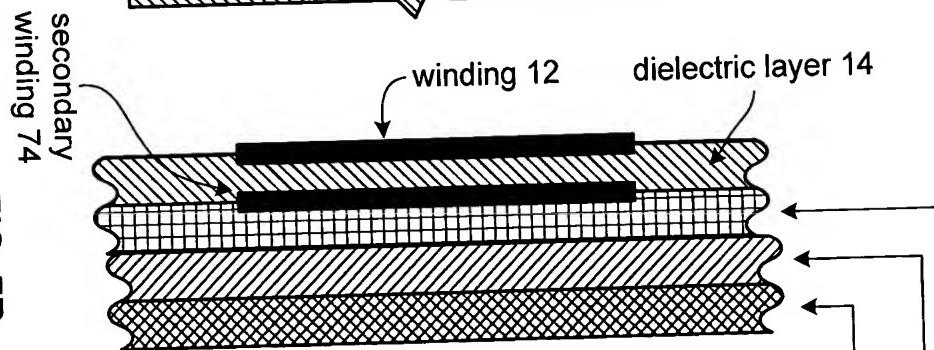


FIG. 5

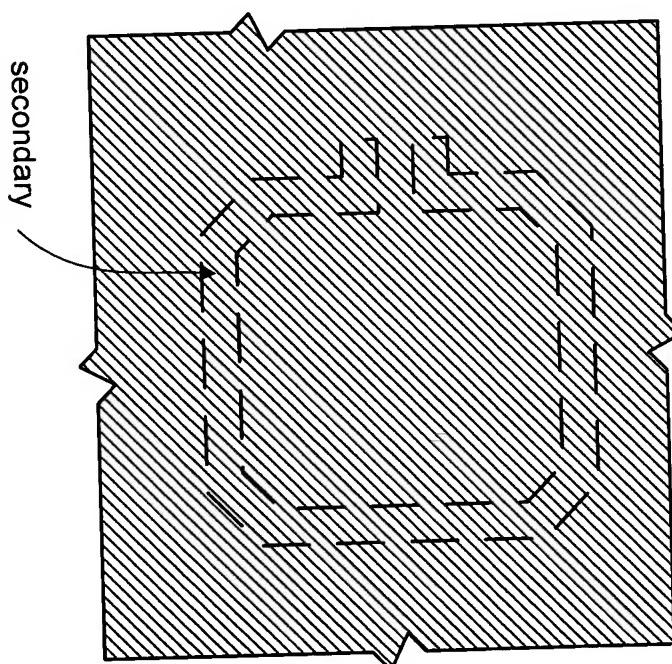




dielectric layer 14
winding 12
FIG. 7A
top view



winding 12
dielectric layer 14
P-well 18 and/or FOX 32
substrate 16
secondary winding 74
FIG. 7B
side view



secondary winding 74
FIG. 7C
bottom view

on-chip transformer 70

on-chip inductor 80

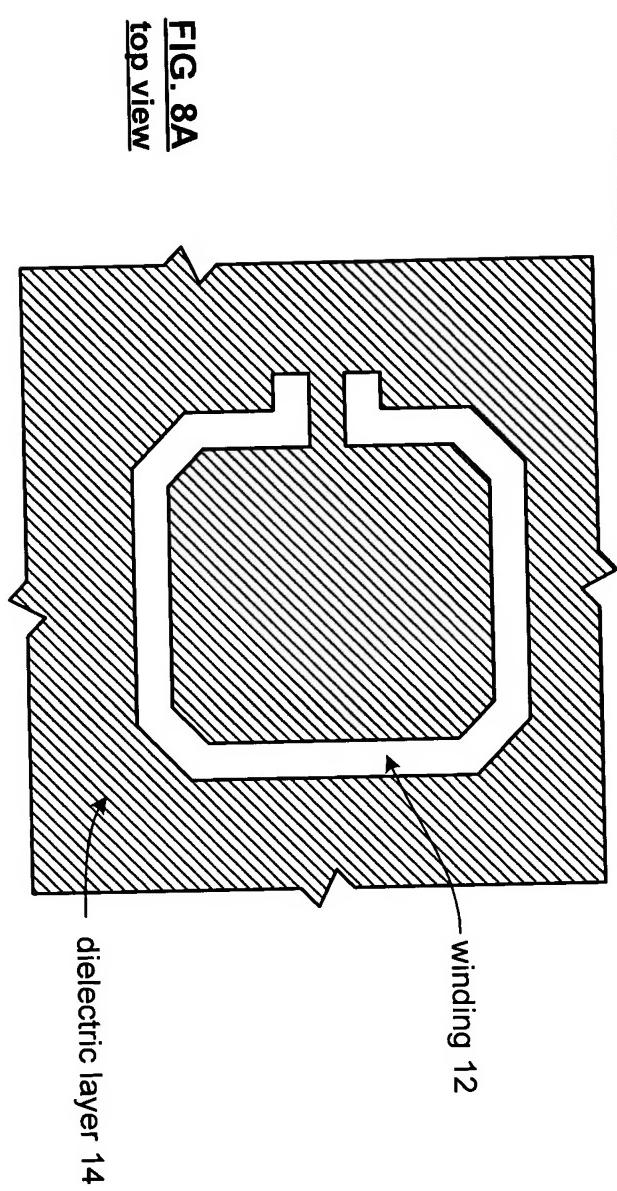


FIG. 8A
top view

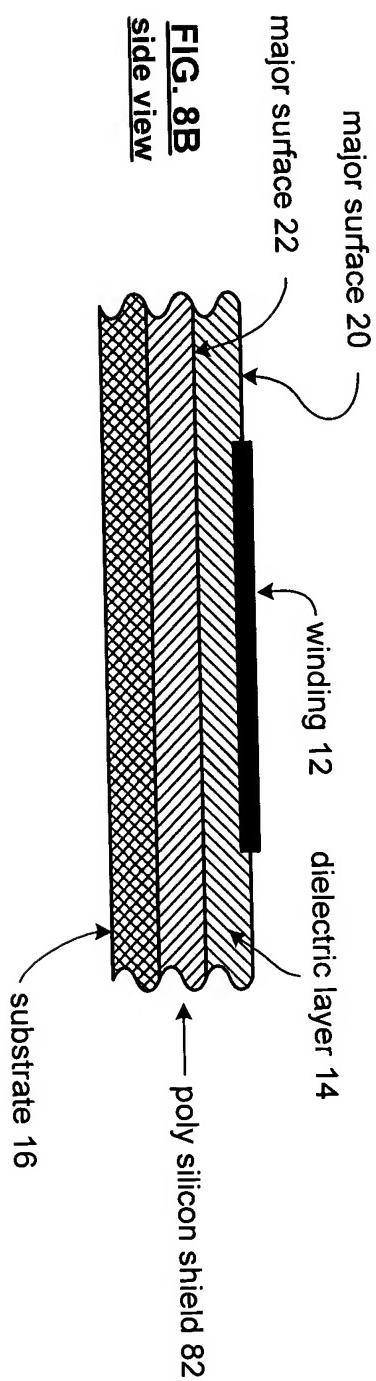


FIG. 8B
side view

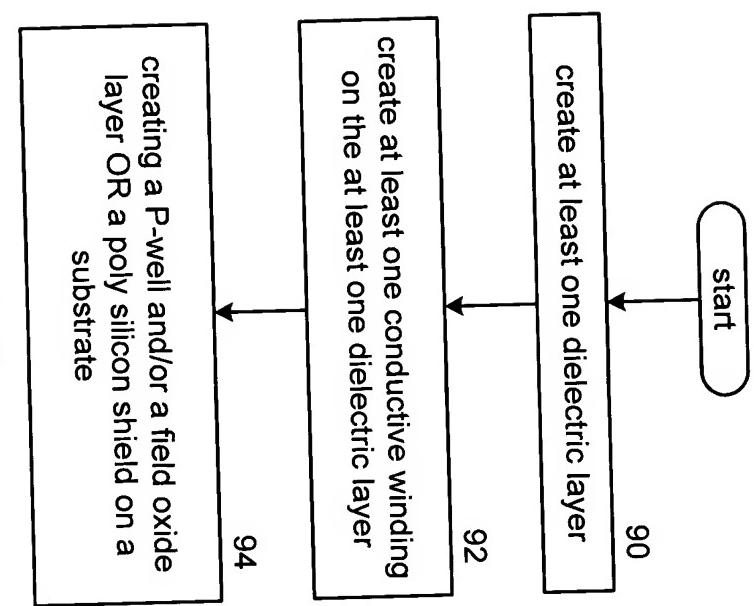


FIG. 9